

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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SHEET 1 of 1

Complete if Known

Application Number	10/573,935
Filing Date	March 23, 2007
First Named Inventor	Hiroyuki TACHIBANA, et al.
Art Unit	2629
Examiner Name	V. Shankar
Attorney Docket No.	MAT-8825US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		JP 08-096714 A (with English abstract)	04/12/96	NEC CORP		<input type="checkbox"/>
		JP 09-245627 A (with English abstract)	09/19/97	MITSUBISHI ELECTRIC CORP		<input type="checkbox"/>
		JP 10-063222 (with English abstract)	03/06/98	HITACHI LTD		<input type="checkbox"/>
		JP 11-297211 A (with English abstract)	10/29/99	NEC CORP		<input type="checkbox"/>
		JP 11-345570 A (with English abstract)	12/14/99	TOSHIBA CORP/TOSHIBA FA SYST ENG CORP		<input type="checkbox"/>
		JP 2001-185034 A (with English abstract)	07/06/01	LG ELECTRONICS INC		<input type="checkbox"/>
		JP 2002-150949 A (with English abstract)	05/24/02	PIONEER ELECTRONIC CORP/SHIZUOKA PIONEER KK		<input type="checkbox"/>
		JP 2004-287174 A (with English abstract)	10/14/04	MATSUSHITA ELEC IND CO LTD		<input type="checkbox"/>

Examiner
Signature

/Vijay Shankar/

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05/06/2008

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